



<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)				ATTY. DOCKET NO. <b>KIM-004</b>		U.S. PATENT APPLICATION NO. <b>10/553,651</b>	
				APPLICANT <b>Masahiko YOSHIMOTO et al.</b>			
				FILING DATE <b>January 17, 2006</b>		GROUP <b>2611</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
/F.A./	"Dynamic Voltage/Clock Frequency Control Method using Feed-forward Analysis for enabling Low Power Video Compression on Multi-regulated Voltage CPU Platform", Kentaro KAWAKAMI et al., Department of Electrical and Electronic Engineering, Kanazawa University						
EXAMINER				DATE CONSIDERED			
/Freshteh Aghdam/				05/06/2010			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.